

# Design and Implementation Low Pass FIR Digital Filter Using Windowing Techniques

Dr. B. Vamsy Krishna <sup>1</sup>, Siva Ganesh Raju Sarikonda <sup>2</sup>, Anusha Varikuti <sup>3</sup>, Veera Venkata Vinay Sanagavarapu <sup>4</sup>, Vijaya Babu Yezarla <sup>5</sup> <sup>1</sup>Assistant Professor in Department of Electronics and Communication Engineering, Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru <sup>2,3,4,5</sup> UG Students with Electronics and Communication Engineering in Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru

### ABSTRACT

A filter can need to have a certain frequency response, or a particular reaction to an impulse, step, or ramp, or mimic an analogue system. Digital filters can be categorised into Finite Impulse Response (FIR) filters & Infinite Impulse Response (IIR) filters depending on the system response. The thesis addresses FPGA low pass FIR filter design. Theoretical and experimental findings conducted FIR low pass filter point to the window design approach as very straightforward and user-friendly due to the presence of well-defined equation. Comparison indicated that the Direct-Form structure technique is simpler and performs better than other typical filter structures whereas Kaiser window gives the minimal main-lobe width and a sharp cut-off indicating smaller transition width. Experimental research of coefficient quantisation reveals a link between the frequency response, number of coefficients, and bit count.

**Key words**: Digital Signal Processing (DSP), Finite Impulse Response (FIR) Filter, Low-Pass Filter, Verilog HDL, Windowing Techniques, Hamming Window, Hanning Window.

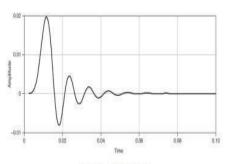
### 1. **INTRODUCTION**

Filtering is the fundamental procedure in digital signal processing. Many electronic devices employ this function to cancel out superfluous or signal-damaging portions of the signal. Filters serve two purposes: signal separation and signal restoration. Signals tainted by noise and interference need separation methods. Breath signal and cardiac signal of the mother will contaminate a gadget measuring the electrical activity of a baby's heart inside the mother's womb. Filters are applied at such periods to isolate the signals and examine them separately. Signal restoration is the method utilised when signal gets distorted. Audio recording done with subpar equipment is filtered to produce better sound signal output than the original it formerly generated. Analogue or digital filters can either solve them. Analogue filters, less expensive, quicker, and with great dynamic range in both amplitude and frequency. By comparison, digital filters show far better performance level.

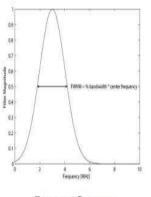
Quality is greater than analogue filters to digital filters can reach performance unique. The filtering issue is approached makes a significant difference. Analogue filters have limits when controls stress accuracy and stability, such as with resistors and capacitors in electronics. By contrast, many people overlook digital filters to improve filtering performance. The emphasis changes signal limits and the processing of theoretical problems. It is a filter in the time domain of the input and output signals telling the DSP. Since it is often produced by the signals of the time pattern at consistent intervals. But this model is not the only way to start. The second most frequent method by which the space matches the sample period. Many more domains are conceivable; nonetheless, time and space are by far the most prevalent. Every linear filter impulse response is a step response



and frequency response. The filter is full information about every one of these responses, but in a different form. Of the three listed, the other two are set 2 and may be computed straight away. Their reactions vary in various contexts to characterise the filter since the three representations, the most essential.



Impulse Response



Frequency Response

Convolution of the digital impulse response with the discrete time signal input is the simplest method to realise a digital filter response. Every linear filter can be created in this way. Filter kernel: impulse response employed in this manner; filter designers provide a unique name. The digital filter is another method as well. Applying a filter weights each input sample in the convolution product and computes it by summing them all. Using the previously computed values, recursive filters and the input points produce these representatives. A collection of coefficients based on the idea of recursive filters replaces a filter kernel. A recursive filter provides motivation by feeding the output of the filter. With exponentially decreasing amplitude, impulse response are sinusoidal. So, they have endless long impulse reactions. So- called recursive filters, or infinite impulse response filters, are IIR filters. Convolution filters, Finite Impulse Response or FIR filters, are seen throughout the course of. When the system's output is motivated by the input impulse response. Input, sometimes known as an edge, is a step in the same way; the step reaction is the output. Comprehensive induction process, so the impulse response is a significant stage in the reaction. A step in the food clears the filter; notice the waveform; or (1) to combine the impulse response: This stage offers two approaches to identify a reaction. While the discrete codes are utilised, mathematically valid codes to be used for the integration of continuous, discrete integration, i.e., a running total. Impulse response frequency response (FFT algorithm employing the technique) DFT appears to be taking

I

# 2. LITERATURE SURVEY

A notable change in optimisation theory has been seen since the start of the nineteenth century. Until this century, classical linear programming and conventional non-linear optimisation methods like Lagrange's Multiplier, Bellman's principle and Pontyagrin's principle were common. Regrettably, the optima on uneven non-linear surfaces cannot be determined using these derivative-based optimisation methods any more. The evolutionary algorithms research community has already suggested one approach to this issue. Genetic algorithm (GA), enunciated by John Holland in the year 1975, is one such popular algorithm which is based on the concept of "survival of the fittest" by Charles Darwin [2]. Holland and his colleagues, particularly Goldberg and Dejong, popularised the GA theory and showed how biological crossings and chromosome mutations might be realised in GA to enhance the solution quality over repeated iterations. Design methods based on Genetic Algorithm (GA) are quite popular for FIR filter synthesis. GA-based minimum phase digital FIR filter design has also been effectively detailed in [3]. The mean squared error (MSE) function is applied to get the optimal-pass band and stop-band responses; the mean absolute error (MAE) is used to maximise the transition band response. According to [4], GA has been used to optimise the design of infinite impulse response (IIR) filters.

Proposed in [5], the IIR filter design under the mixed criterion of H2 norm and  $\infty$  norm is realised using GA. Studies have demonstrated that the filter created by GA outperforms traditional Butterworth filter in either design technique optimisation capacity or designed filter performance. These methods increase the signal to noise ratio (SNR) and bring the frequency domain performance closer to the theoretical one.

Published in [6], a novel approach for creating nonrecursive and recursive frequency sampling filter Investigated is the use of a hybrid real-coded GA to maximise stop band attenuation by means of optimisation of transition sample value. A change lets the coefficient word length be optimised simultaneously, hence lowering total number of design steps and streamlining the design process. The methods can reliably optimise filter with as many as six transition samples. The methods proposed in this article might provide the foundation for combining several of the optimisations. Research on enhancing this integration by means of a binary coded GA to optimise nonlinear phase, quantised coefficient FIR filter are presented together with a GA viewpoint problem analysis [7]. For high speed low complexity filter design, it is common practice to constrain the filters" coefficient to be power of two or a sum of power of two terms (p2), avoiding the full multiplication [8]. Sometimes, tapped connectivity of several sub filters is employed to improve ripple and stop band attenuation performance. Polynomial sharpening methods are an extension of the straightforward cascade structures appropriate for hardware implementation. The design of p2 sharpening filter based on a specific genetic algorithm has been proposed in the above article. The suggested method maximises the sharpening polynomial coefficient stated as p2 terms as well as the FIR sub-filter. This allows getting better performances than the classical p2 design techniques when FIR filters with long impulse response are involved. Using this specific genetic algorithm with a particular free parameters encoding around a set of suitable leading values, allows obtaining a very high reduction of the computational cost. It has been shown in [9] that optimizing both the polynomial and the filter coefficient allow obtaining very good performances; sometimes better that the simple infinite precision sharpening techniques.

The algorithm Particle Swarm Optimization (PSO) was inspired by biological and sociological motivations and can take care of optimality on rough, discontinuous and multimodal surfaces. The aim of this optimization technique is to determine the best-suited solution to a problem under a given set of constraints. Several researchers over the decades have come up with different solutions to linear and nonlinear optimization problems. The optimization problem, now-a-days, is represented as an intelligent search problem, where one or more agents are employed to determine the optima on a search landscape, representing the constrained surface for the optimization problem [8]. In mid 1990s, Eberhart and Kennedy enunciated an alternative solution to the complex non-linear optimization problem by emulating the collective behaviour of bird flocks, particles and called their brainchild Particle Swarm Optimization (PSO) [10]. Particle swarm optimization techniques can be used to design infinite impulse response (IIR) filter [11]. It is observed that the particle swarm algorithms are able to converge very rapidly when the error surface is relatively constant. This is the fundamental advantage of particle swarm



algorithm for online adaptive filtering. Though standard PSO exhibits a fast convergence initially, it fails to improve further because swarm quickly becomes stagnant, converging to a suboptimal solution. However, with the same set of algorithm parameters, the Modified Particle Swarm Optimization (MPSO) particles do not stagnate, allowing it to reach the noise floor along with the GA. Particle Swarm Optimization techniques has also been used to design frequency sampling FIR digital filter. By applying PSO to optimize transition sample values, the maximum stop band attenuation in FIR filter is obtained. It has been experimented that to design FIR filter, PSO is more superior to GA not only in the convergence speed but also in the performance of filter [12]. Stable IIR digital filters with non-standard amplitude characteristics can be designed using particle swarm optimization techniques [14]. PSO has been the newly elaborated techniques for optimization of multi-modal functions. Design of IIR digital filters with non-standard amplitude characteristics which considerably differ from typical Butterworth, Chebyshev, and Cauer approximations, is possible using presented method. The IIR digital filter with linearly falling amplitude characteristics is designed with the use of proposed method. The filter is stable and fulfils all prescribed design assumptions. Differential Evolution (DE) algorithm has been emerged as a very competitive form of evolutionary computing more than one decade ago. The first written article on DE appeared as a technical report by R. Storn and K. V. Price in 1995. One year later, the success of DE was demonstrated at the First International Contest on Evolutionary Optimization in May 1996, which was held in conjunction with the 1996 IEEE International Conference on Evolutionary Computation (CEC). DE finished third at the 1st ICEO, which was held in Nagoya, Japan. In quick times, DE has turned out to be the best evolutionary optimization algorithm for solving real valued test function. Price presented DE at the second International contest on Evolutionary Optimization in 1997 and it turned out as one of the best among the competing algorithms. In 2005 CEC competition on real parameter optimization, on 10- D problems classical DE secured 2nd rank and a Self adaptive DE variant called SaDE secured 3rd rank although they performed poorly over 30-D problems. Later depending upon many improved DE variants some optimization techniques has been proposed in the period 2006-2009 such as improved SaDE. jaDE

,Opposition Based DE (ODE), DE with global and local neighbourhoods (DEGL) and so on A linear phase finite impulse response (FIR) filter has been designed using Differential Evolution Particle Swarm Optimization (DEPSO) technique as mentioned in. Here two different fitness functions have been studied and experimented, each having its own significance. The first study considers a fitness function based on the pass-band and stop-band ripple, while the second study considers fitness function based on the mean squared error between the actual and the ideal filter response. DEPSO seems to be one of the promising tools for FIR filter design especially in a dynamic environment where filter coefficient have to be adapted and fast convergence is of paramount significance. In recent times, it has been observed that the DE algorithm based on reverse gene can also be applied for the design of digital filter [38]. It can produce new chromosomes in every generation by combining reversed gene of special chromosome into a single entity.

### **3.PROPOSED SYSTEM**

Finite impulse response filters, or FIR filters, are non-recursive type filters with finite period impulse response. IIR, or Infinite Impulse response, are recursive filters with infinite period impulse response. A non-recursive type filter is one whose output sample relies on both the current input sample and the prior input sample. Recursive type filters use one or more of their outputs as an input. FIR filter's impulse response is of finite period since it settles to zero in finite time. FIR filter is significant since it can have no phase distortion. FIR filters are preferred over IIR in digital processing applications. With precise linear phase, FIR filters are always stable and simple to construct. FIR filters are also less susceptible to quantisation mistakes in the filter.



### **FIR Filter**

In digital signal processing systems, finite impulse response (FIR) filter is quite significant. Its output sample relies on the current input sample and past input sample. Thus, it is a non-recursive filter. It is steady and may simply be constructed with precisely linear phase.

The difference equation for the FIR filter is given as

y(n) = b0 x(n)+b1x(n-1)

+.....+ bN x(n-N)

It defines the relation of the input signal to the output signal.

It can also be expressed as = 0 otherwise

Where wa(n) is the frequency response

The equation for Hamming window can be obtained by substituting  $\alpha = 0.54$  in equation 1.

 $wH\alpha(n) = 0.54 + 0.46$ 

for  $-(N - 1)/2 \le n \le (N-1)/2$ .

= 0 otherwise

### C. Kaiser Window

Kaiser Window generates a sharp central peak. It reduces side lobes and transition band is narrowed.

The Kaiser window is given by

$$y(n) = \sum_{i=0}^{N} b_i x(n-i)$$
  $\frac{Io[a\sqrt{1-(2n/N-1)^2}]}{Io(n)}$ 

Where x(n) is the input signal, y(n) is the output signal, bi is the filter coefficient and N is the filter order

wk(n) =

for  $|n| \leq N-1$ 2

### **B.** Hamming Window

Hamming window is a type of raised cosine window. The window sequence of cosine window is of the form

$$w\alpha(n) = \alpha + (1 - \alpha) \cos^{2\pi n}$$
 —  
N-1

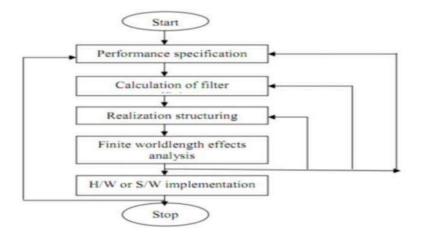
for  $-(N - 1)/2 \le n \le (N-1)/2$ . = 0 otherwise

Where  $\alpha$  is the adjustable parameters and Io(x) is the modified zeroth- order Bessel function.[13]

I



# **FIR Filter Design**



Summary of design stage for digital filter

#### Fig 1: Proposed System

accurate signed multiplier. The delay of the 8-bit multiplier can be lowered, though, by cutting the multiplier

### 3. RESULTS AND DISCUSSION

Magnitude and phase responses of a 20th order digital low pass filter using Hamming, Rectangular, and Kaiser window functions are observed and investigated as shown in Figures 5.1, 5.2 and 5.3. The input signal frequency is below the cut off frequency i.e., 2 MHz, so we get to observe the output signal using Hamming window. The input signal frequency is below the cut off frequency horizontally in the centre and separating it into two smaller 8×4 groups.

i.e., 2 MHz, so we get to observe the output signal using rectangular window. The input signal frequency is below the cut off frequency i.e., 2 MHz, so we get to observe the output signal using Kaiser Window. The input signal frequency is above the cut off frequency i.e., 2 MHz, so we cannot observe the output signal using Hamming window. The input signal frequency is above the cut off frequency i.e., 2 MHz, so we cannot observe the output signal using rectangular window. The input signal frequency is above the cut off frequency i.e., 2 MHz, so we cannot observe the output signal using rectangular window. The input signal frequency is above the cut off frequency is above the cut off frequency i.e., 2 MHz, so we cannot observe the output signal using Kaiser Window.

I



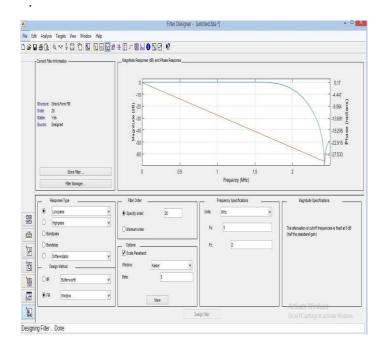


Figure 2: FIR LPF using Hamming window

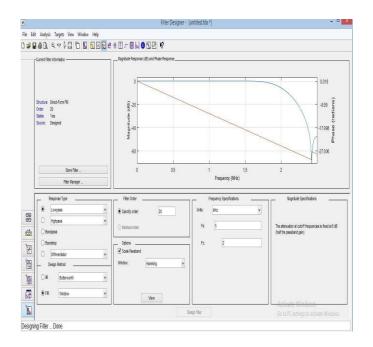


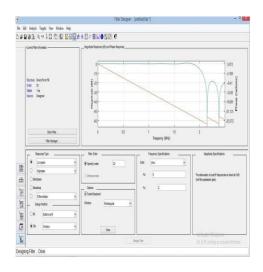
Figure 4: FIR LPF using Kaiser Window

T



### Table 1: Filter Specifications

Parameters	Values
Filter type	Low pass
Design method	FIR Window ( $\beta = 5$ for
	Kaiser)
Filter order	20
Cut off	2 MHZ
frequency	
Sampling	5 MHZ
frequency	



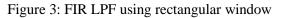




Figure 5: Simulation result of the FIR LPF using Hamming window Figure 6: Simulation result of the FIR LPF Using rectangular window

T



Untitled 7° × filter_re	ctangular_tb.v	x																			? 6
0 1 0 0	X 🕯 🕅	H	ť.	± •	1		H														1
											3,800.1	100 ns 1	.000 ze								
Name	Value	13,2	10 15		13,40	11		13,601	11		13,800 1	5	14,0	0 15		14,20	15	1	4,400	15	14,6
) 🛚 write_ena. (0:3240)		1,1,1	,1,1,1	1,1,1,	,l,l,	1,1,1	1,1,	,1,1,	1,1,0,	0,0,	,0,0,0,	,0,0,1	1,0,0,0	,0,0,0	,0,0,1	,0,0,	9,0,0,1	1,0,4,	0,0,0,	,0,0,0,0	,0,0.
) 🕊 write_don (0:3240)	0,0,0,0,0,0,0,0,0	0,0,0	,0,0,0	0,0,0,	1,0,0,	0,0,0	0,0,0	,0,0,	0,0,1,	0,0,	,0,8,0,	,0,0,1	1,0,0,0	,0,0,0	,0,0,1	,0,0,	0,0,0,	1,0,1,	0,0,0,	0,0,0,0	,0,0.
) 🛚 filer_cut_d(0.3240)	000000000	0.0,0	.0,8.0	1,0.0,	1.0,0.	0,0.0,	0.0,0	.0,0.	0,0.0,	0.0,	.0,0.0,	1.0,0.1	1,0.0,0	0,0.0	,0.0,1	.0,0.	0,-3.1	509500	445206	6e-17,-	0 02.
i dit	1																				
tok_enable	1																				
Teset	0	+	-	1	_		_	-			1		-		1	_		_	-	_	
) <b>V</b> file <u>r</u> in(630)	-4.6166304619	L_											1								L
) 🖏 fiter_out[63.0]	-4.8116987283																				
🕷 wrte_enable	0	+608		-	-400 1		7	-200 1	7			100.01	0 ns		5	400 m	-1	٦	00 ms	4 -	1800

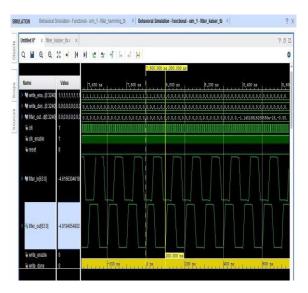


Figure 7: Simulation result of the FIR LPF using Kaiser Window Figure 8: Simulation result of the FIR LPF at cut off frequency using Hamming Window

Table 2: Comparison of 20th-order fir LPF using different window function

Window technique	Filter order	No. of side lobes	Transition width
Hamming window	20	1	Moderate
Rectangular window	20	2	Narrowest
Kaiser Window	20	1	Variable

T



itler_rectangular_t5.v	× Untitled 1*	X ? (
ଦ ≌ ଢ ଢ	8 <b>4</b> K	N 12 2 14 1 1 1 1
		11. 151889 Wz
Name	Value	11 us 13 us 14 us 15 us 16 us
) 🖣 file_out_d(0.3183)	-8.9143536294	-R. 934358629458374-18,-0.005555580170388,0.0595772357429,0.095345143231788,-0.04790578688115495,-0.18911714071691
i di	1	
🕯 dk_enable	1	
in reset	0	
) ₩itter_n(620)	46071155676	
) Miller_od(i30)	0.02291531072	
ije to_ent	1	
🖟 srcDone	0	
k stilDone	Ú.	
e tesFalure	6	



Figure 9: Simulation result of the FIR LPF at cut off frequency using Rectangular Window

Figure 10: Simulation result of the FIR LPF at cut off frequency using Kaiser Window

# 4. CONCLUSION

FIR filters, which are stable and have linear phase properties, are extensively used in wired and wireless communications, video, audio processing, and mobile devices. From software level to the hardware level, this work presents a new design strategy for an efficient FIR digital filter. The main goal is to include all the areas used in the efficient hardware realisation of filters i.e. design approach, selection of structure and the algorithm to lower the arithmetic cost of FIR filtering.

Using feed forward pipelining retiming techniques, low pass FIR filter is built. Broadcast and non-broadcast low pass digital FIR filter architectures are used to verify the rise in area and speed using the present broadcast, non broadcast FIR filter. The results of synthesis and simulation are discussed in full. When compared to all other constructed designs, feed forward non-broadcast FIR filter (Kaiser, where \u03b2=5) structure has quickest speed

# REFERENCES

1. [R.Dhivya and Dr.P.Brindha ," Design and implementation of FIR filter using retiming technique", International Journal of advance research , ideas and innovations in technology , Volume3, Issue3 , 2017.

2. Aksha Joy and C.S.Vinitha

,"Folding and register minimization transformation on DSP filter", 5 th IEEE Uttar Pradesh Section Conference on Electrical and Computer Engineering (UPCON),2018.

3. Karni Ram and M.Kumar

","Implementation of Word Level Parallel Processing Unfolding Algorithm using VHDL", IJEAT, Vol .8, No.4, 2019.

4. Jacinta Potsangbam and M.Kumar, "Design and Implementation of Combined Pipelining and Parallel Processing Architecture for FIR and IIR filter Using VHDL", International Journal of VLSI Design

and Communication Systems, Vol. 10, No. 4, 2019.

5. L Kholu Phimu and M.Kumar, "VLSI Implementation of Area Efficient 2-Parallel FIR Digital Filter", International Journal of VLSI Design and Communication Systems, Vol.7, No.5/6, 2016.

6. Pramod Patali and Shahana Thottathikkulam Kassim "High throughput and energy efficient FIR filter architectures using retiming and two level pipelining", Third International Conference on Computing and Network Communications (CoCoNet'19).

7. Aarti Sharma and Sanjay Kumar "VLSI Implementation of Pipelined FIR Filter" International Journal of Innovative Research in Electrical, Electronics, InstrumentationAnd Control Engineering Vol. 1, Issue 5, August 2013.

8. Parhi K. K. (2007), 'VLSI Digital Signal Processing Systems Design and Implementation', New York, NY, USA: Wiley.

9. Ravinder Kaur and Ashish Raman "Design and Implementation of High Speed IIR and FIR Filter using Pipelining" International Journal of Computer Theory and Engineering, Vol. 3, No. 2, April 2011.

10. Jaya Bar and Madhumita Mukherjee "A Comparative Study on Direct form -1, Broadcastand Fine grain structure of FIR digital filter" International Journal of Electronics, Communication & Soft Computing Science and Engineering ISSN: 2277-9477, Volume 2, Issue 5

11. B. K. Mohanty and P. K. Meher, "A High-Performance FIR Filter Architecture for Fixed and Reconfigurable Applications," IEEE Transactions on Very Large Scale Integration (VLSI) Systems, vol. 24, no. 2, pp. 444-452, Feb. 2016

12. Yu-Chi Tsao and Ken Choi, "Area- Efficient VLSI Implementation for Parallel Linear- Phase FIR Digital Filters of Odd Length Based on Fast FIR Algorithm" IEEE Transactions On Circuits And Systems—ii: Express Briefs, Vol. 59, No. 6, June 2012

# Author's Profile:



**Dr. B. Vamsy Krishna** is presently working as an Assistant Professor in the Department of Electronics and Communication Engineering at Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru, specializing in Electronics and Communication Engineering. With over 20 years of teaching experience, he has been instrumental in shaping the careers of numerous students in ECE.

He holds a strong academic background and has contributed significantly to the field through her expertise and mentorship. His dedication to research and teaching in the field of electronics has made a lasting impacting academia, inspiring students to excel in the ever-evolving world of VLSI.





**SIVA GANESH RAJU SARIKONDA** is a B.Tech student specializing in Electronics and Communication Engineering at Seshadri Rao Gudlavalleru Engineering College. he is passionate about exploring emerging technologies and continuously enhancing her skills through hands-on challenges. He has completed internships in VLSI, Embedded Systems and Artificial Intelligence and Data Visualization under AIMER Society, gaining practical experience, and also completed internship in Cyber Security Analyst under SmarBridge Educational Service Pvt.Ltd. Additionally, he has earned NPTEL certification in Introduction to Internet of Things, Digital VLSI Testing and Sensors and Actuators. She is skilled in PCB (Printed Circuit Board) Making.



**JONNAKUTI ANUSHA** is a B.Tech Student specialising in Electronics and Communication Engineering at Seshadri Rao Gudlavalleru Engineering College. She has completed internships in embedded systems, IoT, and VLSI, gaining expertise in both hardware and software. Certified by NPTEL in IoT, he is skilled in Power BI, Verilog HDL, and Python, with a strong foundation in digital design and functional verification. He has experience in PCB design, object detection using Roboflow, and visual question-answering with Hugging Face. His expertise includes sensors, actuators, and embedded system interfacing.



**SANAGAVARAPU VEERAVENKATA VINAY** is a B. Tech student specializing in Electronics and Communication Engineering at Seshadri Rao Gudlavalleru Engineering College. He is passionate about exploring emerging technologies and continuously enhancing his skills through hands-on challenges. He has completed internship in Artificial Intelligence under Aimers Society. Additionally, He has earned NPTEL certification in Introduction to Internet of Things 2.0. He is skilled in PCB (Printed Circuit Board) Making.





**YAZARRLA VIJAYA BABU** is a B. Tech student specializing in Electronics and Communication Engineering at Seshadri Rao Gudlavalleru Engineering College. He is passionate about exploring emerging technologies and continuously enhancing his skills through hands-on challenges. He has completed internship in Artificial Intelligence under Aimers Society. Additionally, He has earned NPTEL certification in Introduction to Internet of Things 2.0. He is skilled in PCB (Printed Circuit Board) Making.