

Research Paper on SIMULATION OF MULTIPULSE CONVERTOR USING CONTROL RECTIFIER

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Abstract - Power quality concerns are now a major concern area of research in the power sector. With the invention of technology, it is now possible to keep the power sector free from pollution. Over the past few years, much effort has been put into reducing overall harmonic distortion using a variety of concepts and applications. This project deals with the reduction of total harmonic distortion using a multi-pulse AC to DC conversion scheme. Each such converter provides 6pulse AC for DC conversion, so to create more sets of 6pulse systems, a coherent phase-shift is required and therefore produces multi-pulse systems with appropriate phase-transfer angles. Is gone. The performance of a multi-pulse converter is received for overall harmonics distortion (THD) in the pre-deliver modern-day. All simulations were done for the same rating for all multi pulse converters configurations. Results are obtained for arbitrary converters for R load.

1.INTRODUCTION

Power machine harmonic distortion has existed for the reason that early 1900s, so long as AC strength itself has been to be had. The earliest harmonic distortion issues were associated with 0.33 harmonic currents produced through saturated iron in machines and transformers, so-known as ferromagnetic masses. A better know-how of power gadget harmonic phenomena can be done with consideration of a few essential concepts, especially, the nature of nonlinear masses, and the interplay of harmonic currents and voltages inside the electricity machine. By definition, harmonic (or nonlinear) hundreds are the ones devices that evidently produce a non-sinusoidal modern-day whilst energized by a sinusoidal voltage supply Each "waveform" on proper, represents the version in on the spot cutting-edge over time for 2 special masses each energized from a sinusoidal voltage supply. Both contemporary waveforms had been produced through turning on a few kind of load device. In the case of the contemporary on the left, this tool changed into probable a resistance heater. Power electronic switching device in conjunction with nonlinear loads causes serious harmonic problem in power system due to their inherent property of drawing harmonic current and reactive power from AC supply mains. They cause voltage unbalance and neutral currents problem in power system. With the distortion of current and voltage waveform due to presence of harmonic effect the power system equipment that are connected to maintain steady and reliable power flow in the power system.

2. LITERATURE SURVEY

- 1. "The Impacts of Harmonics Reduction on THD Analysis in HVDC TransmissionSystem using Threephase Multi-Pulse and higher Level Converters, Gbadega Peter . A A.K Saha.978-1-7281-0369-3/19/\$31.00 ©2019 IEEE In this paper eliminating the glitches confronted by High Voltage Direct Current (HVDC) transmission systems such as converting station and harmonic cost can therefore make it more realistic. Generally, total harmonic distortion can simply be lessen up to the allowable limits, justby increasing the pulses number in a multi-pulse converter.
- 2." Power Quality Enhancement Using Current Injection Technique in a Zigzag Configured Autotransformer Based 12-Pulse Rectifier" R Kalpana,. Khimavath Sai Chethana This paper proposes a DC side circuit configuration that improves the harmonic suppressionability of a 12-pulse diode bridge rectifier (DBR) using a zigzag configured autotransformer. TheDC side circuit uses a 1-phase DBR along with interphase transformer which generates therequired circulating current thereby modifies the DC currents at the DBR output, in turn shapesthe input line current near to a sine wave. The proposed 1-phase DBR is connected in parallelwith the load which enables to reuse the harmonic energy thus improving the energy conversion efficiency.
- " Modeling and Simulation of Multi-Pulse Converter for Harmonic Diminution", Urmil Desai, Darshan Rajesh Vo ra 978-1-5090-4715-4/17/\$31.00 ©2017 IEEE The concern of power quality now days is a major anxious area of research in the power sector. With the innovation in the technology now it is possible to keep power sector free for thereduction of Total Harmonic Distortion using different concepts and applications. This researchpaper deals with the diminution of Total Harmonic Distortion using Multipulse AC to DCConversion scheme. Every such converter provides 6-pulse AC to DC conversion, so in order tocreate more sets of 6- pulse systems, a consistent phase-shift is required and hence with properphaseshifting angle, 6, 12, 24, and higher pulse systems have been produced. The performanceadvance of multi pulse converter is achieved for total harmonics distortion (THD) in supplycurrent. All the simulations have been done for similar ratings for all the multi pulse

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converters configurations. The results are obtained for uncontrolled converters for R Load.

4." Harmonic Reduction Technology at DC Link in Star-Connected-Autotransformer-Based Multi-Pulse Rectifier", Zhe Liu, Fangang Meng, 978-1-5386-2894-2/17/\$31.00 ©2017 IEEE. In this paper, In order to improve the harmonic reduction ability of the multipulse rectifier, thispaper proposes a multi-pulse rectifier based on harmonic reduction technology at DC link. The proposed rectifier employs two diode bridges, each followed by a Boost converter. By controlling the inductor current of Boost converter, the input line current of the proposed rectifier can be approximated to sinusoidal waveform. A star-connected autotransformer is used to be phase-shift transformer and the winding of the autotransformer are interconnected, which can significantly decrease the equivalent kVA rating of the transformer and improve the power density. This paper also calculates the theoretical inductor current waveform of Boost converter when the input line current is sinusoidal, and presents the applicable waveform.

3. PROBLEM DEFINITION

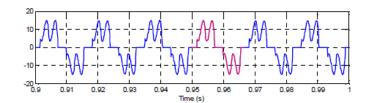
Project The present work is an effort in the direction of analyzing the distinctive multi-pulse AC to DC converters in solving the harmonic trouble in a 3-section converter system. The impact of growing the number of pulses on the performance of AC to DC converters has been analyzed. For performance comparison the most important elements taken into consideration are the ripple percent, shape aspect and the overall harmonic distortion (THD). The consequences of load variation on multi-pulse AC to DC converters have also been investigated

4.RESULT

These harmonic spectrums are acquired whilst induction motor operates underneath light load (20% of full load) and full load conditions. Manifestly, for six-pulse converter, 5th and seventh order harmonics are dominant.

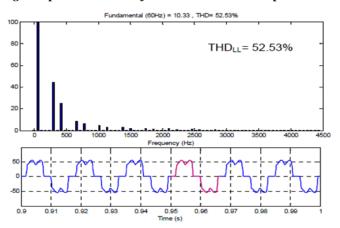
Table I Comparison of Simulated Power Quality Parameters of the Dtcimd Fed from Different Ac–Dc Converters

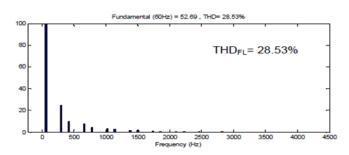
Sr. No.	Topology	% THD of Vac	AC Mains Current ISA (A)		% THD of ISA, at		Distortion Factor, DF		Displacement Factor, DPF		Power Factor, PF		DC Voltage (V)	
			Light Load	Full Load	Light Load	Full Load	Light Load	Light Load	Full Load	Full Load	Light Load	Full Load	Light Load	Full Load
1	6-pulse	5.64	10.33	52.69	52.5	28.5	0.88	0.87	0.94 8	0.95	0.98	0.98	616.	607.6
2	36-pulse	1.86	10.53	52.23	3.26	1.88	0.99	0.99	0.99	0.99	0.99	0.99	611.	605.7



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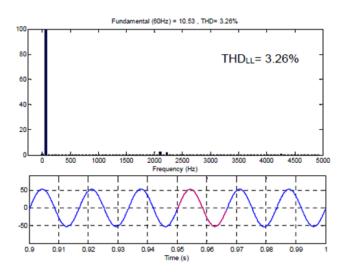
Fig. 2 Input current waveform and its harmonic spectrum

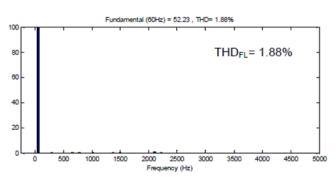




Input cutting-edge thd and electricity thing variations are also shown in fig, for 6-pulse, and 36-pulse ac-dc converters. Effects display that the enter cutting-edge similar to the proposed configuration has an almost cohesion power component. Furthermore, in the worst case (mild loads) the current thd has reached under 4% for the proposed topology

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Special energy excellent indices of the proposed topology under one of a kind loading situations are shown in desk ii. Consequences show that even beneath load variations, the 36-pulse converter has an improved overall performance and the current thd is always much less than 4% for all loading situations.

Table 2. Comparison Of Power Quality Indices Of Proposed 36- Pulse Ac-Dc Converter

Load	THD	(%)	CF	DF	DPF	TPF	RF (%)	
(%)	IS	vs	of IS	DF	DPF	IPF		
20	3.26	0.83	1.413	0.9995	0.9986	0.9981	0.002	
40	2.61	1.16	1.414	0.9996	0.9977	0.9974	0.005	
60	2.06	1.39	1.414	0.9997	0.9971	0.9968	0.005	
80	1.91	1.65	1.414	0.9997	0.9969	0.9966	0.002	
100	1.88	1.86	1.414	0.9997	0.9969	0.9966	0.002	

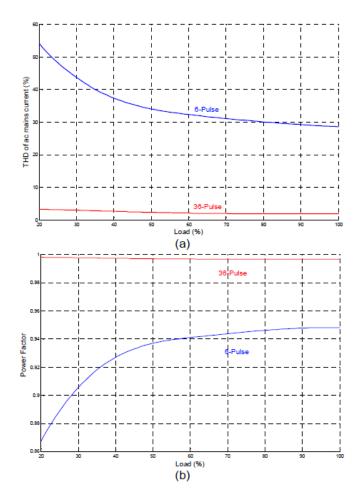
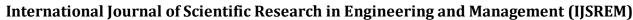


Fig4 Variation of (a) THD and (b) power factor with load

CONCLUSION:

With the growth in wide variety of pulses of converter improves the power high-quality through reducing the enter modern harmonics from the ac mains. Hence pulse multiplication approach can play a vital function in electricity first-class development in diverse packages including energy distribution networks, **HVDC** transmission systems, critical commercial and industrial hundreds etc. The most important objective of the present work is to analyze the overall performance of managed multi-pulse converter. These converters are studied in phrases of harmonic spectrum of AC most important modern, Output voltage Ripple and Form Factor. It is Conclusion that in trendy that will increase the quantity of pulse in multi-pulse converter the overall performance of the converters is extensively progressed.



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REFERENCES:

- [1] V. Nedic and T. A. Lipo, "Low-cost current-fed PMSM drive system with sinusoidal inputcurrents," IEEE Trans. Ind. Appl., vol. 42, no. 3, pp. 753–762, May/Jun. 2006.
- [2] W. Farrer, "Significant source harmonic reduction achieved using di- rect parallel connection of two 6-pulse converters," Proc. Inst. Electr. Eng.—Electr. Power Appl., vol. 153, no. 2, pp.167–176, Mar. 2006.
- [3] K. Mukherjee, S. SenGupta, T. K. Bhattacharya, A. K. Chattopadhyay, and S. N. Bhadra, "Simplified analytical averaged model of a thyristorized commutatorless series motor," IEEETrans. Ind. Appl., vol. 42, no. 6, pp. 1508–1515, Nov./Dec. 2006.
- [4] A. I. Maswood, A. K. Yusop, and M. A. Rahman, "A novel suppressed- link rectifier-inverter topology with near unity power factor," IEEE Trans. Power Electron., vol. 17, no. 5, pp. 692–700, Sep. 2002.
- [5] R. Naik, N. Mohan, M. Rogers, and A. Bulawka, "A novel grid interface, optimized forutility-scale applications of photovoltaic, wind-electric, and fuel263 International Journal ofScience and Research (IJSR), India Online ISSN: 2319-7064 Volume 2 Issue 4, April 2013www.ijsr.net cell systems," IEEE Trans. Power Del., vol. 10, no. 4, pp. 1920–1926, Oct. 1995.